WO 2005/031940 PCT/SE2004/001316

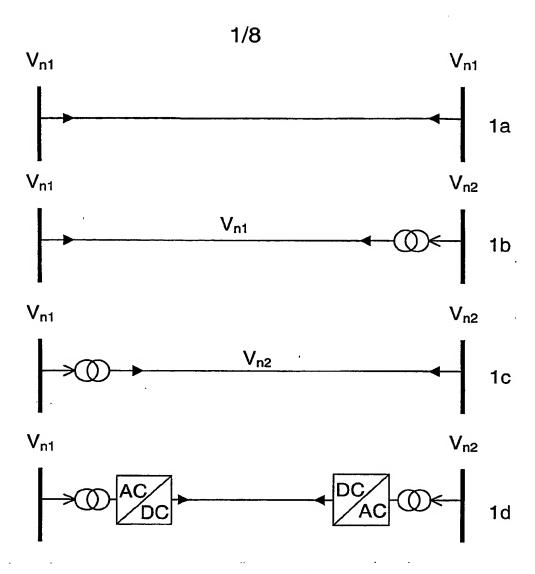


Figure 1a-d Prior Art

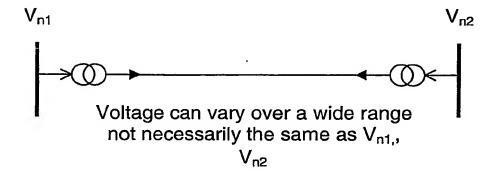


Figure 2

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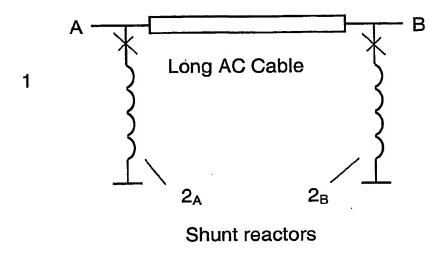


Figure 3 Prior Art

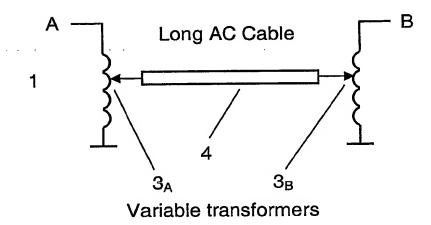


Figure 4

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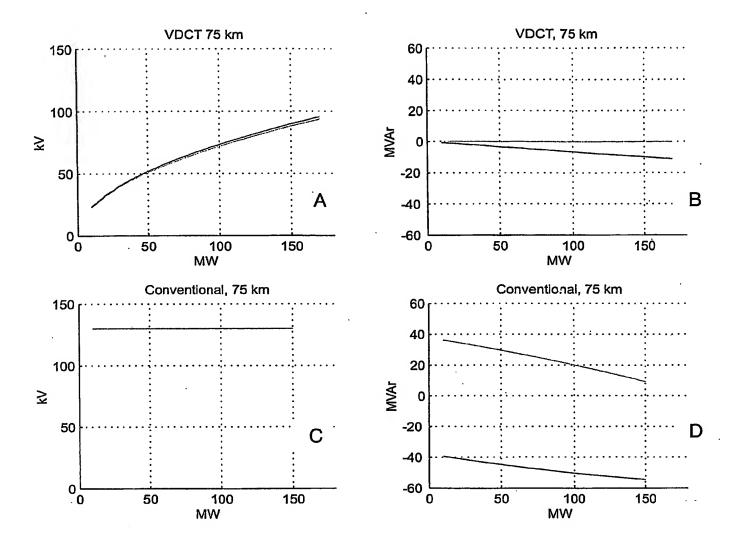


Figure 5

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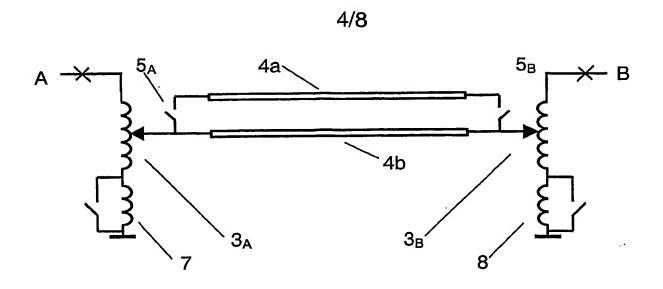


Figure 6

Cable Voltage Optimization

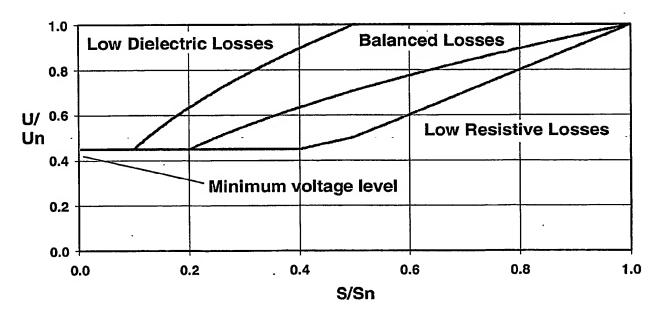


Figure 7

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Cable Voltage Optimization

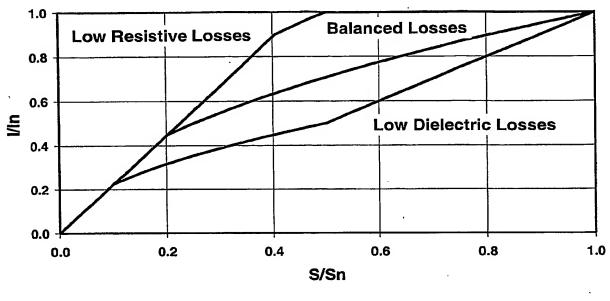


Figure 8

Cable Voltage Optimization

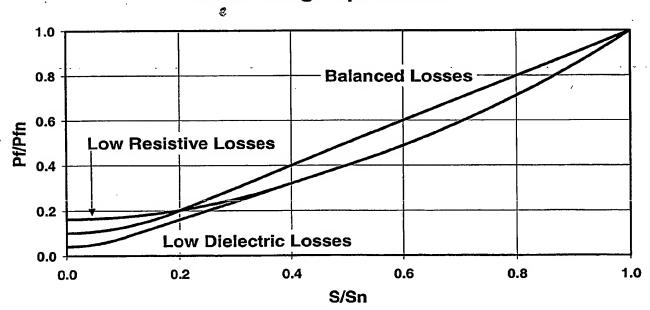


Figure 9

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Low Dielectric Losses

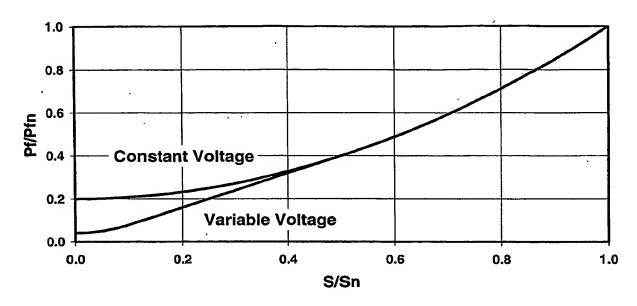


Figure 10

Balanced Losses

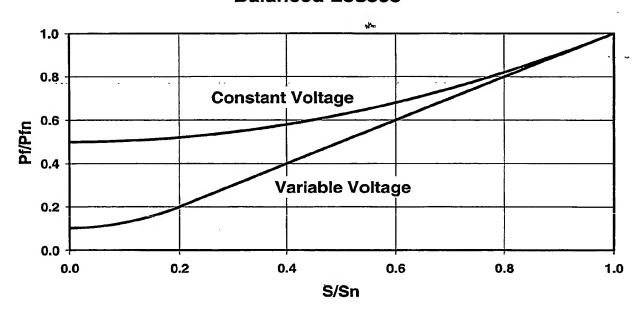


Figure 11

High Dielectric Losses

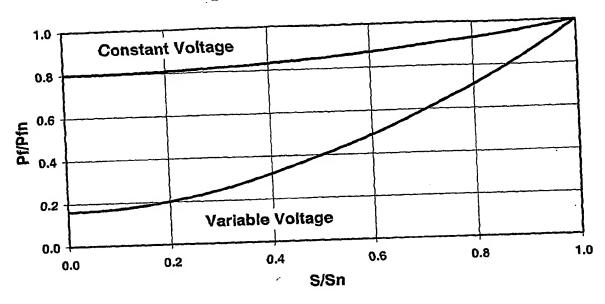


Figure 12

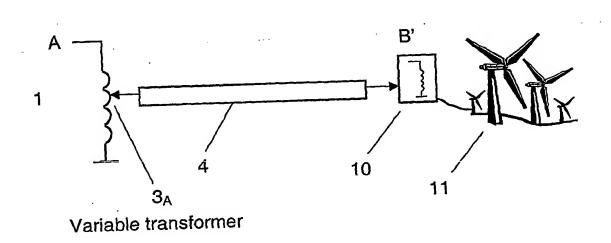


Figure 13

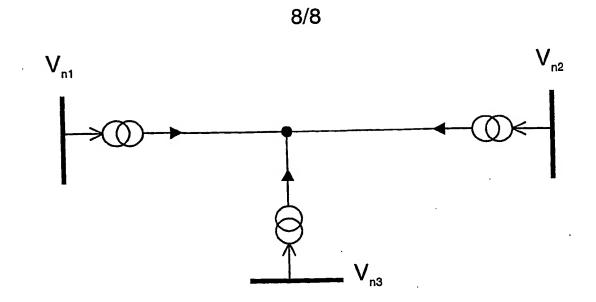


Figure 14

